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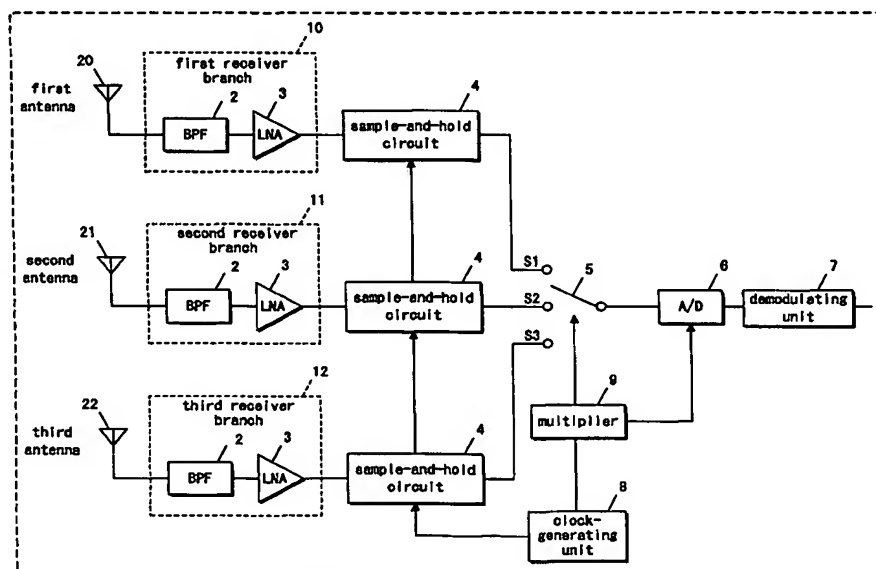
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(57) Abstract: A receiver including a plurality of receiver branches operable to receive signals, a plurality of sample-and-hold circuits (4), each of which is connected to corresponding one of the receiver branches at the output thereof, a switch (5) connected to the sample-and-hold circuits at outputs thereof, and a demodulating unit (7) connected to the switch (5) at the output thereof. Each of the sample-and-hold circuits (4) is operable to extract a discrete value from the output from corresponding one of the receiver branches. The switch (5) is operable to allow output signals from the sample-and-hold circuits (4) to be selectively fed out of the swit



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